

REPLACEMENT SHEET

FIG. 1

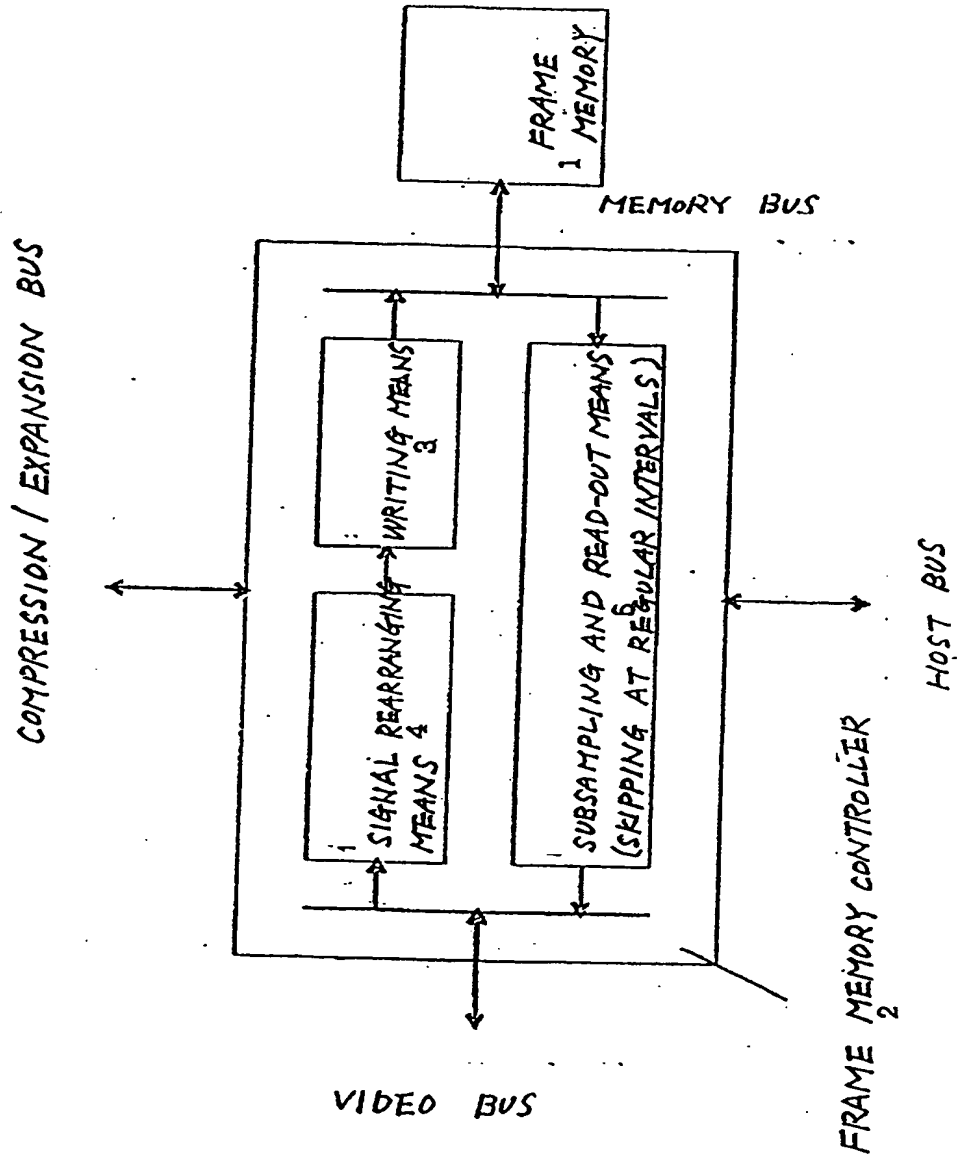
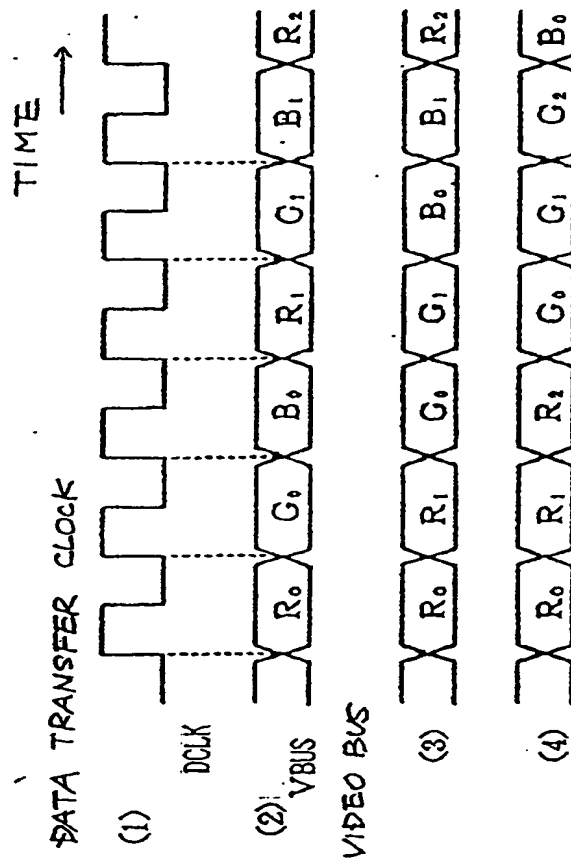


Fig. 2



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FIG. 3

	0	1	2	3	4	5	
0	R_{00}	R_{01}	G_{00}	G_{01}	B_{00}	B_{01}	
1	R_{10}	R_{11}	G_{10}	G_{11}	B_{10}	B_{11}	
2	R_{20}	R_{21}	G_{20}	G_{21}	B_{20}	B_{21}	
3	R_{30}	R_{31}	G_{30}	G_{31}	B_{30}	B_{31}	

FIG. 4

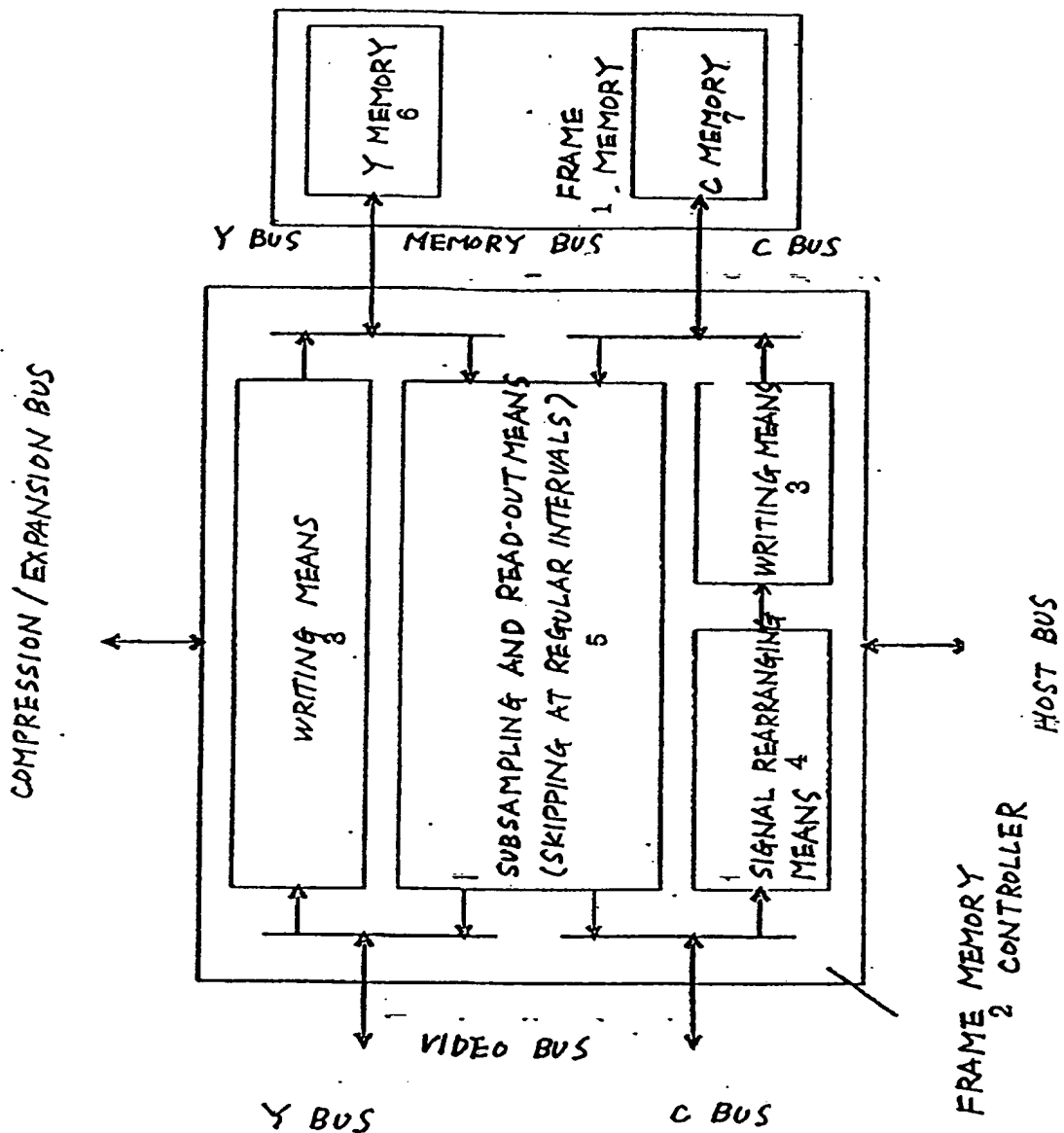


FIG. 5

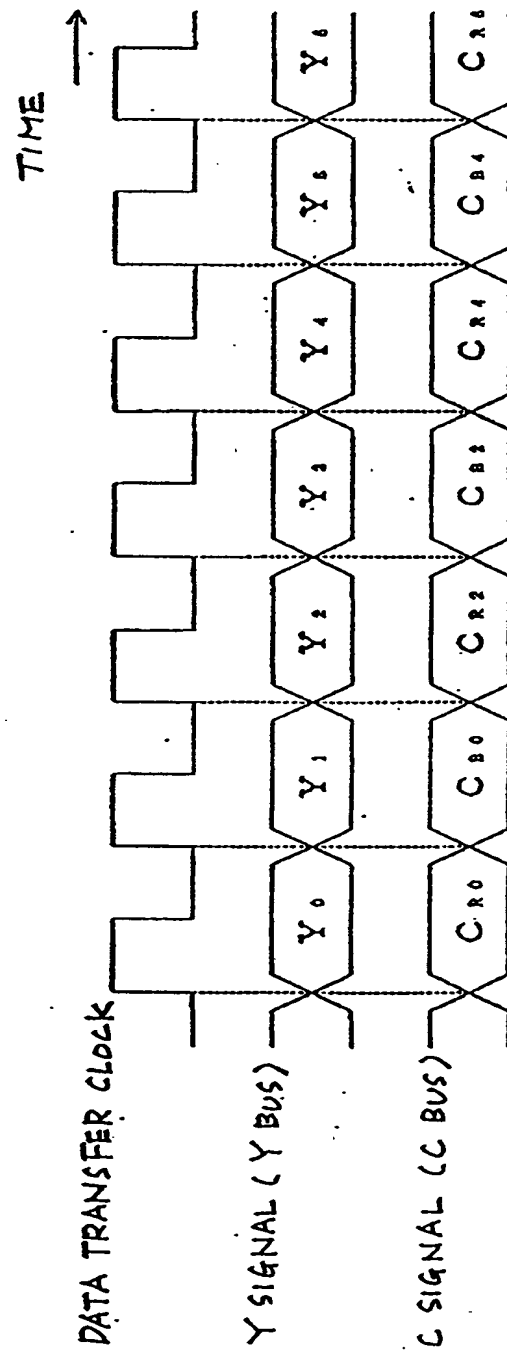
HORIZONTAL ADDRESS

	0	1	2	3	4	5	6	7	
0	C _{B00}	C _{B02}	C _{R00}	C _{R02}	C _{B04}	C _{B06}	C _{R04}	C _{R06}	
1	C _{B10}	C _{B12}	C _{R10}	C _{R12}	C _{B14}	C _{B16}	C _{R14}	C _{R16}	
2	C _{B20}	C _{B22}	C _{R20}	C _{R22}	C _{B24}	C _{B26}	C _{R24}	C _{R26}	
3	C _{B30}	C _{B32}	C _{R30}	C _{R32}	C _{B34}	C _{B36}	C _{R34}	C _{R36}	

VERTICAL ADDRESS

STORAGE ADDRESS OF C_B AND C_R SIGNAL (C MEMORY)

FIG. 6



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FIG 7

HORIZONTAL ADDRESS

	0	1	2	3	4	5	6	7	
0	C R 0 0	C R 0 2	C B 0 0	C B 0 2	C R 0 4	C R 0 6	C B 0 4	C B 0 6	
1	C R 1 0	C R 1 2	C B 1 0	C B 1 2	C R 1 4	C R 1 6	C B 1 4	C B 1 6	
2	C R 2 0	C R 2 2	C B 2 0	C B 2 2	C R 2 4	C R 2 6	C B 2 4	C B 2 6	
3	C R 3 0	C R 3 2	C B 3 0	C B 3 2	C R 3 4	C R 3 6	C B 3 4	C B 3 6	

VERTICAL ADDRESS

STORAGE ADDRESS OF C_B AND C_R SIGNAL (C MEMORY)

FIG. 8

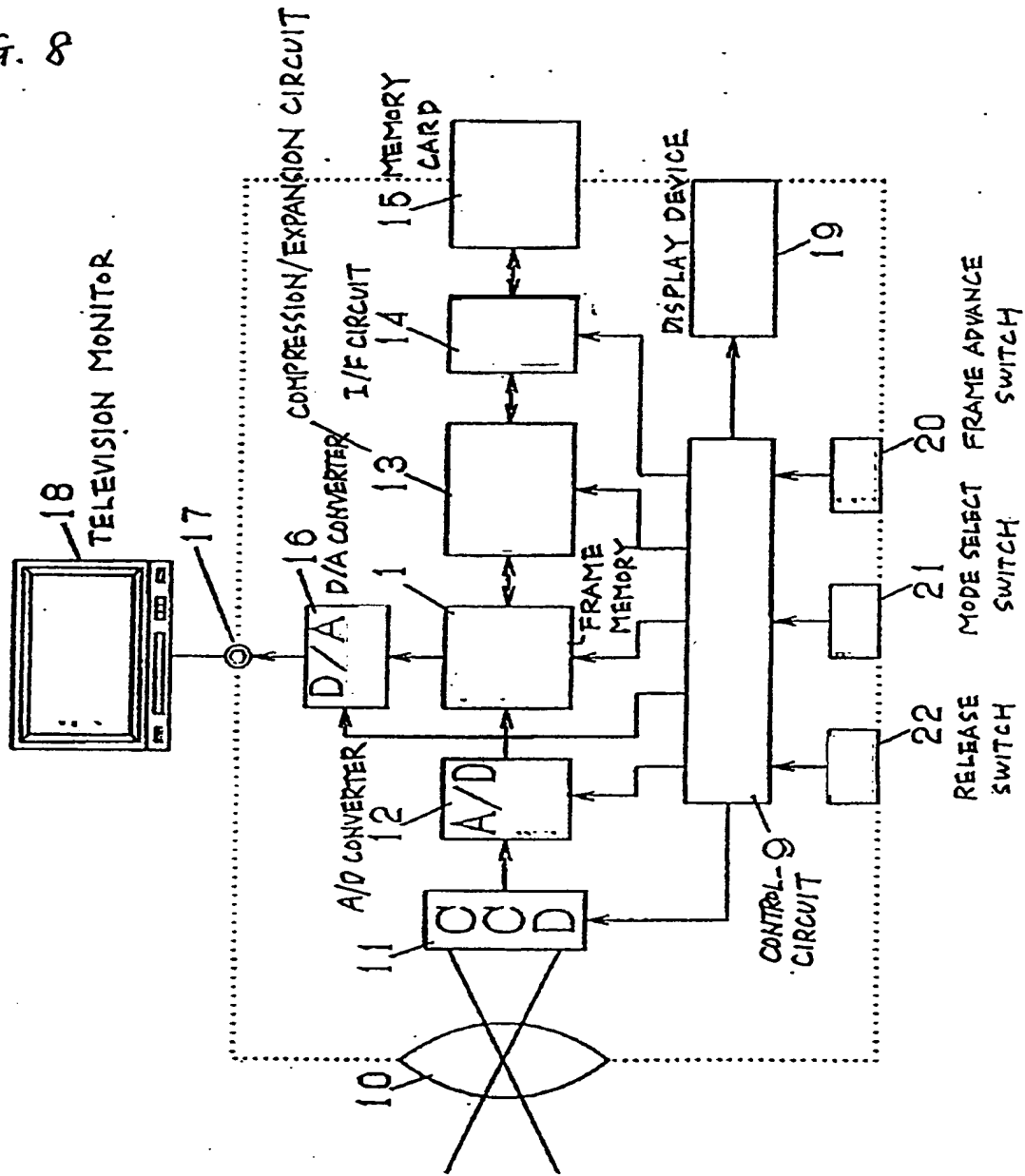


FIG. 9

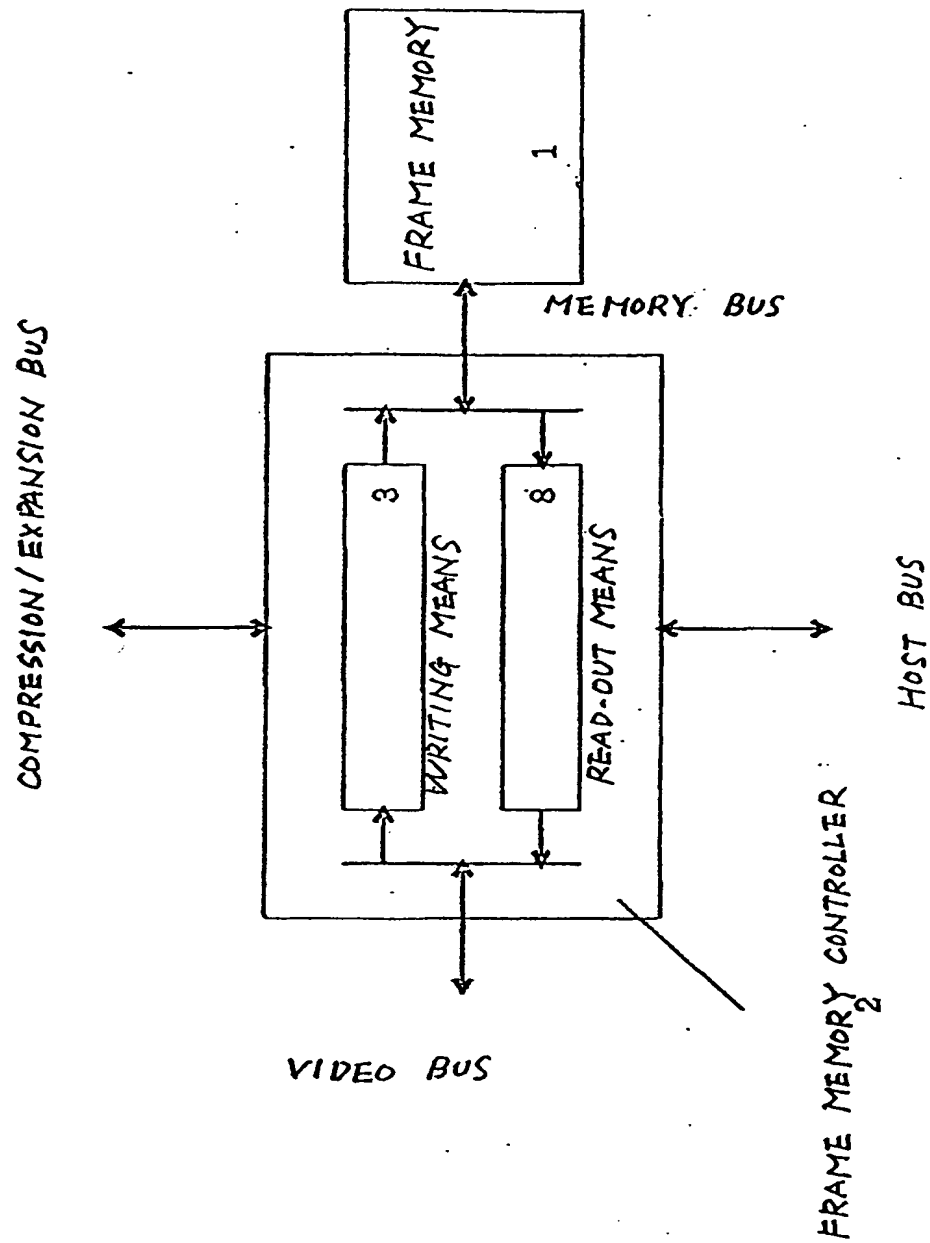
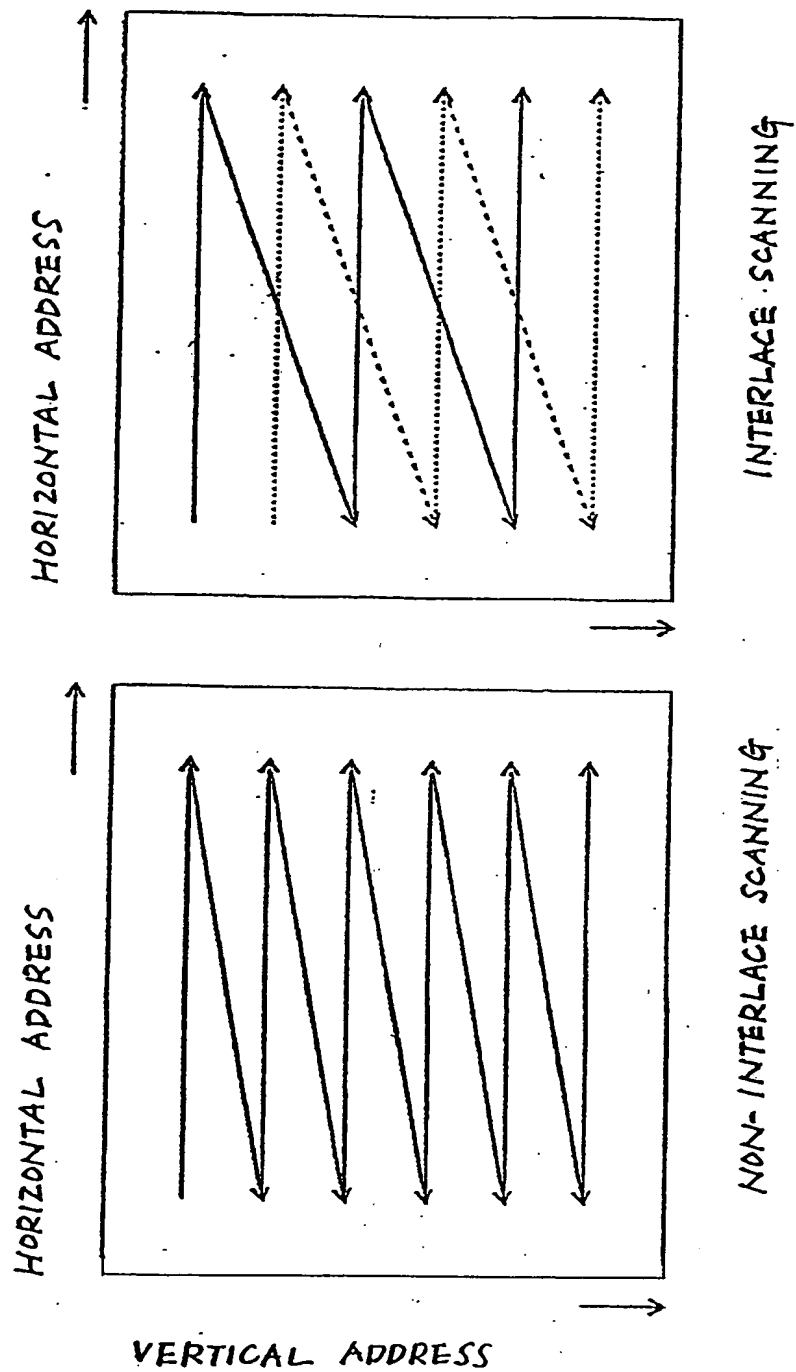


FIG. 10



MANNER OF STORING IMAGE SIGNAL ON MEMORY UPON VIDEO INPUT

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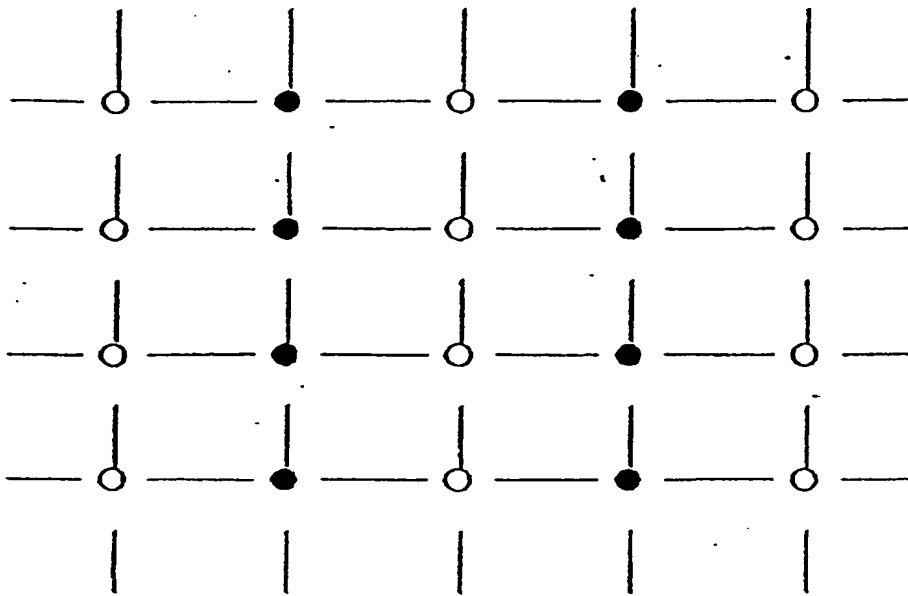
FIG. 11

G	B	G	B	G	B	G	B	G
R	G	R	G	R	G	R	G	R
G	B	G	B	G	B	G	B	G
R	G	R	G	R	G	R	G	R
G	B	G	B	G	B	G	B	G
R	G	R	G	R	G	R	G	R
G	B	G	B	G	B	G	B	G
R	G	R	G	R	G	R	G	R

SINGLE DISC COLOR FILTER HAVING
BAYER ARRANGEMENT

REPLACEMENT SHEET

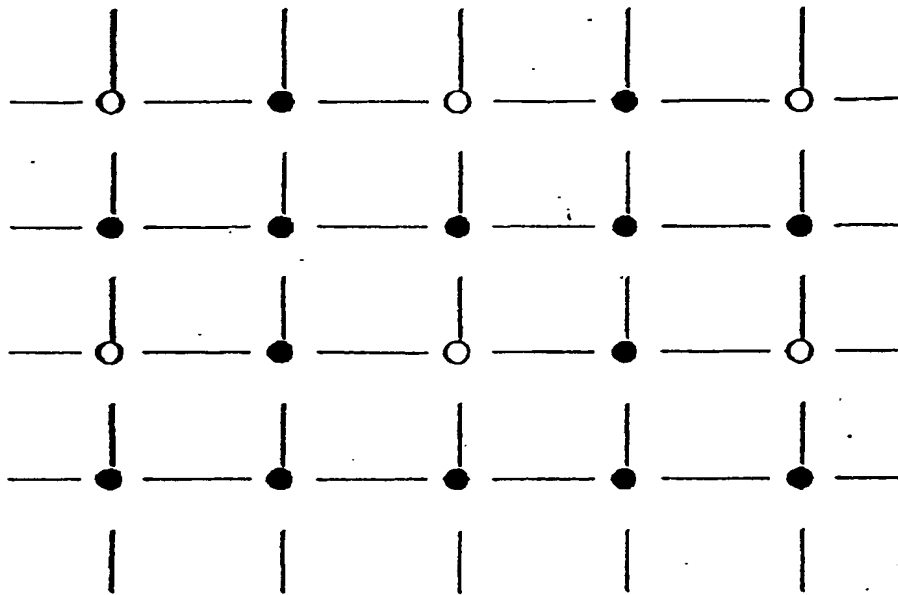
FIG. 12



SAMPLING COORDINATES OF $YCbCr$ 4:2:2
IMAGE SIGNAL

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FIG. 13



SAMPLING COORDINATES OF YCbCr 4:1:1
IMAGE SIGNAL

FIG. 14

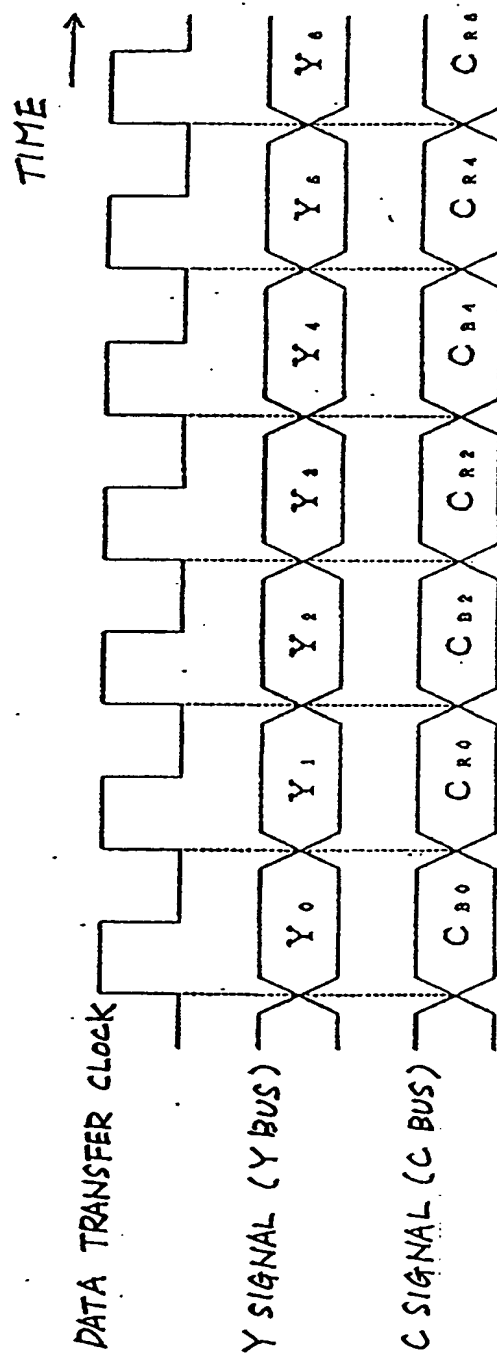
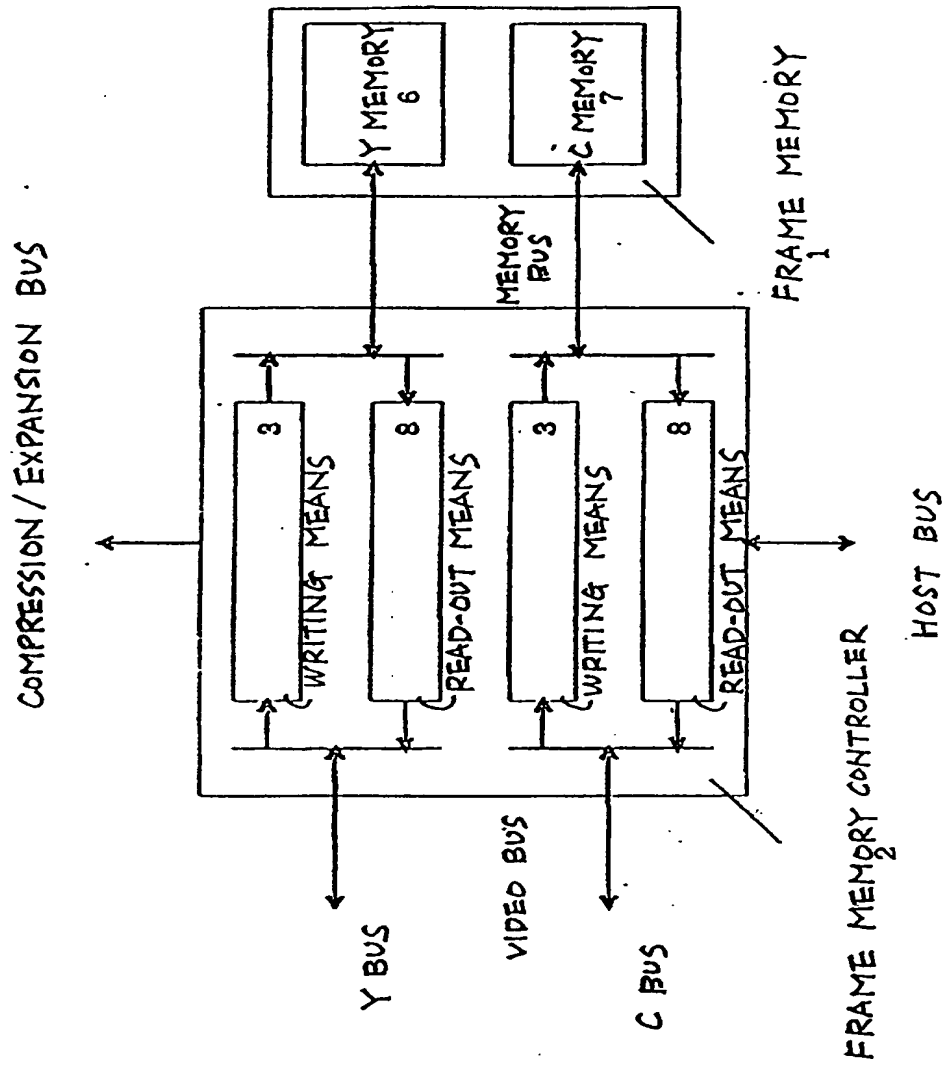
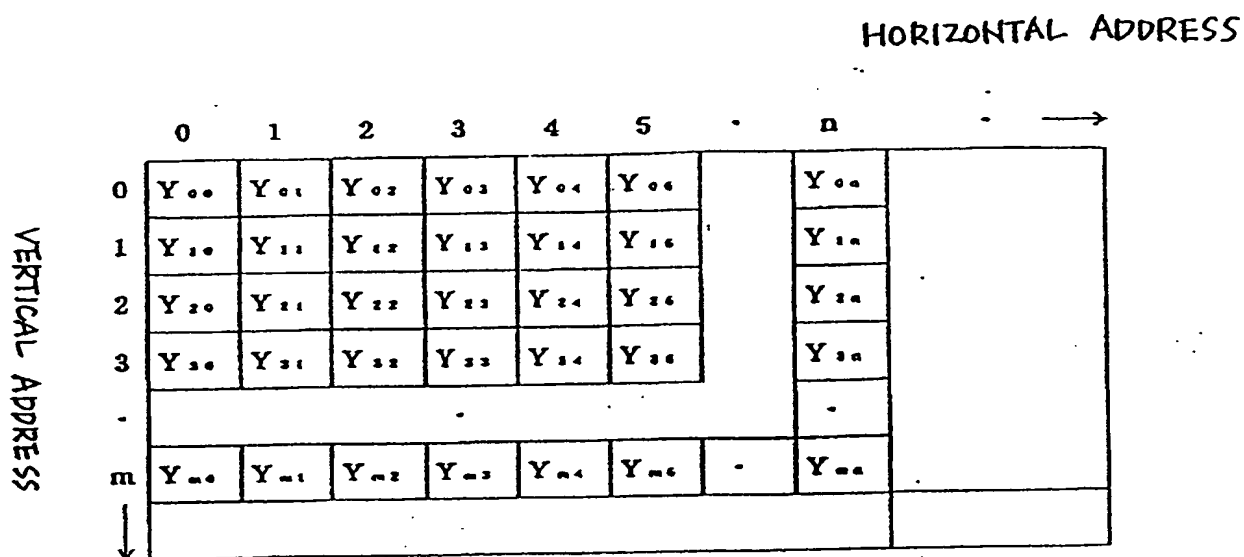


FIG. 15



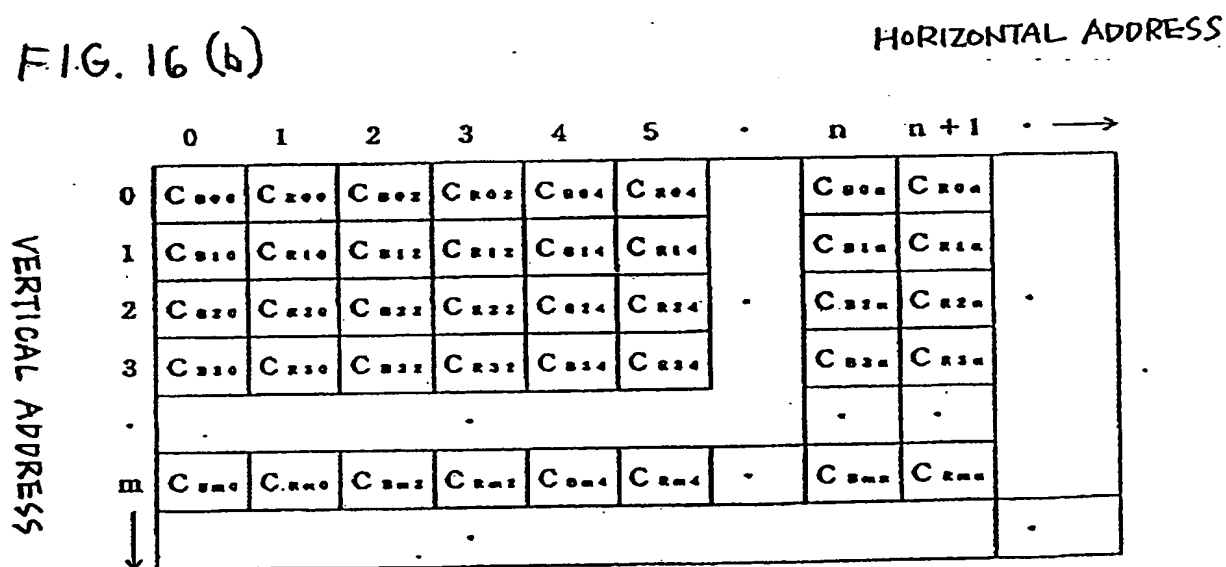
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FIG. 16(a)



STORAGE ADDRESS OF Y SIGNAL (Y MEMORY)

FIG. 16(b)



IN THIS CASE, n IS INTEGER

STORAGE ADDRESS OF C_B AND C_R SIGNAL (C MEMORY)

FIG. 17

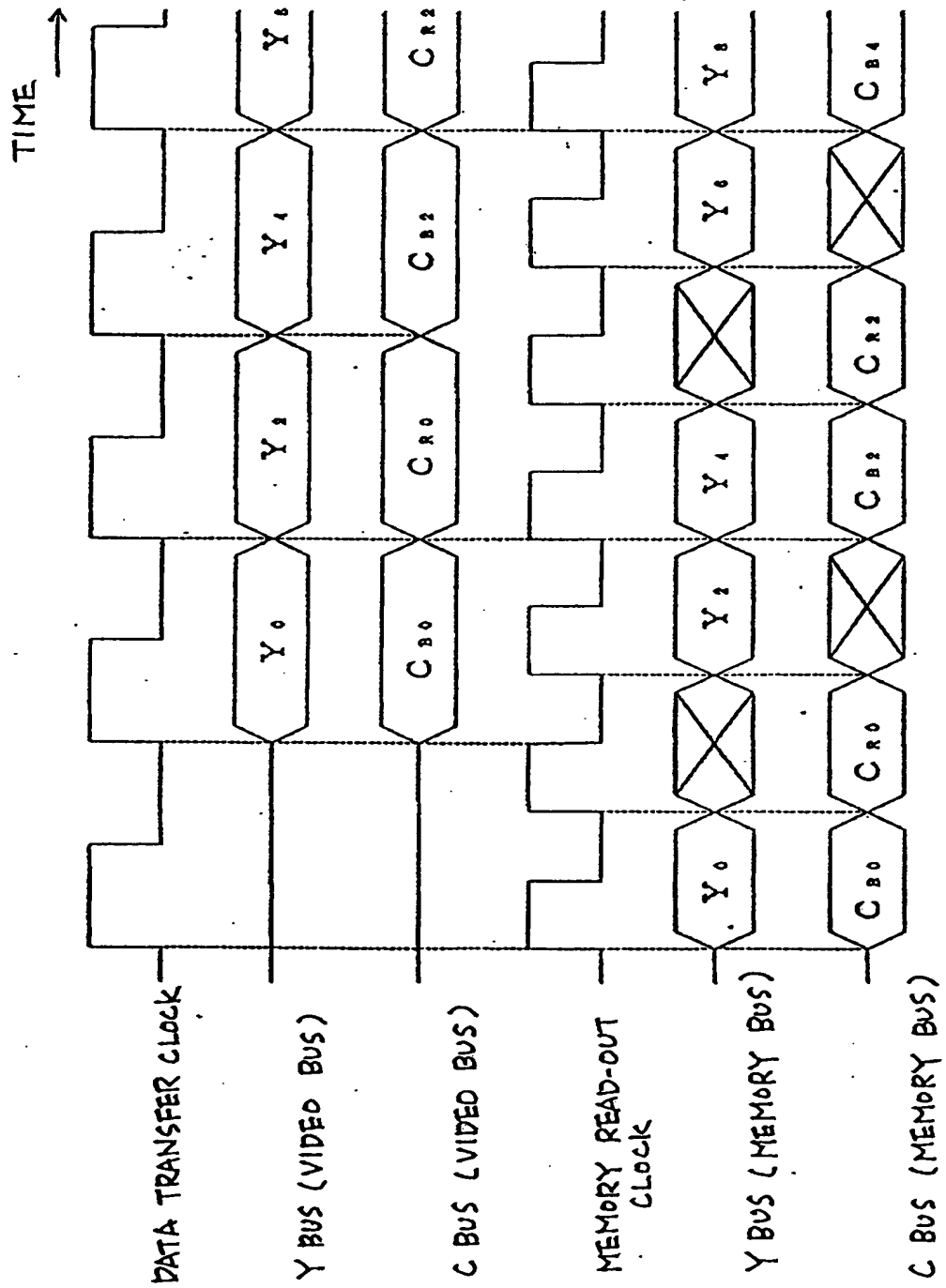


FIG. 18

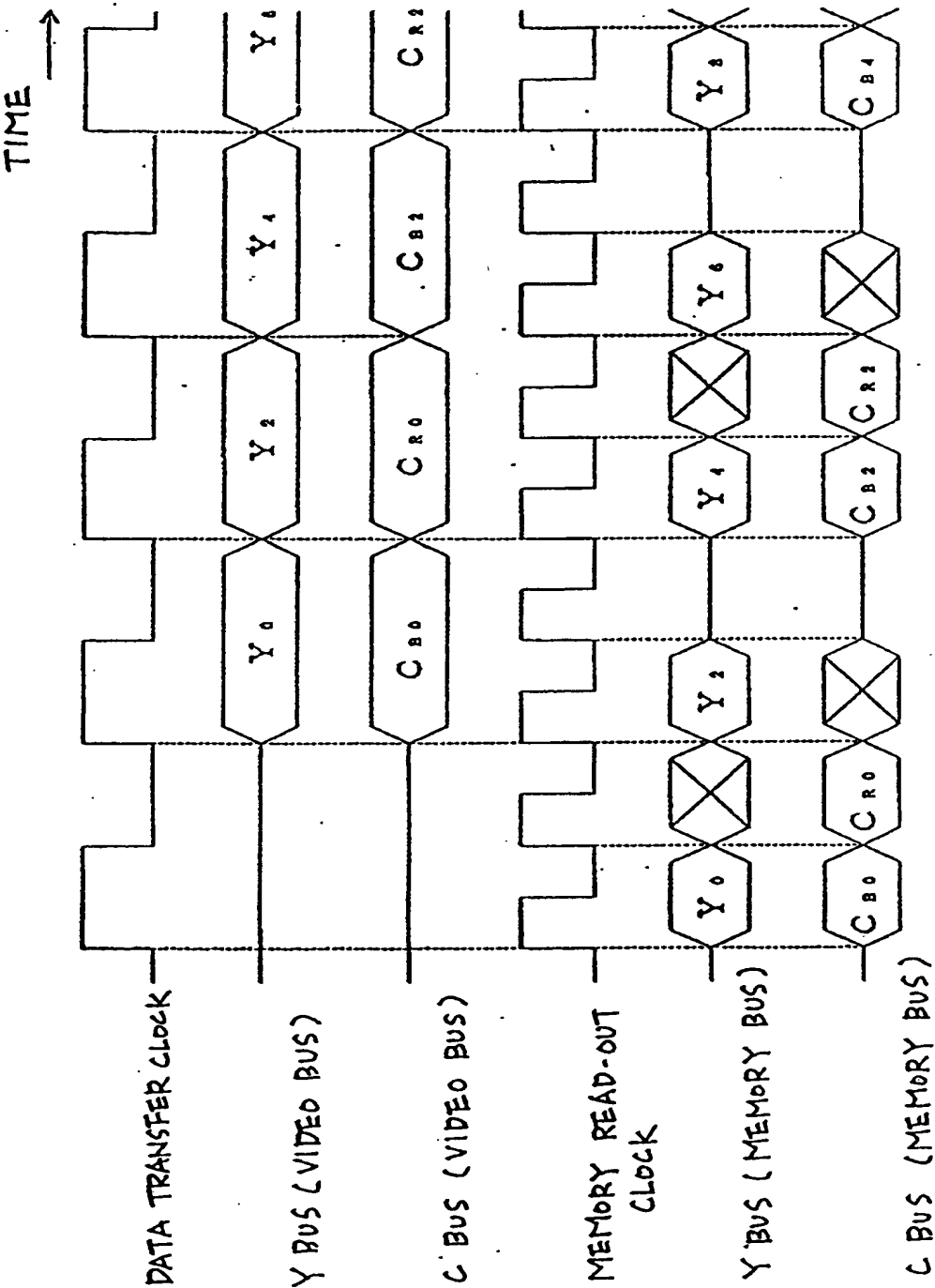


Fig- 19

